

Notice of References Cited

Application/Control No.

10/713,052

Applicant(s)/Patent Under
Reexamination
MOOSBURGER ET AL.

Examiner

SHAMBHAVI PATEL

Art Unit

2128

Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,345,239	02-2002	Bowman-Amuah, Michel K.	703/6
*	B	US-2002/0059054	05-2002	Bade et al.	703/20
*	C	US-6,970,814	11-2005	Ashley et al.	703/14
*	D	US-6,102,961	08-2000	Lee et al.	716/1
*	E	US-6,687,710	02-2004	Dey, Aparna	705/36R
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Delpasso et al. "Virtual Simulation of distributed IP-based Designs" ACM 1999.
	V	Wilsey, Philip. "Web-Based Analysis and Distributed IP" 1999.
	W	Reutter et al. "An Efficient Reuse System for Digital Circuit Design." 1998.
	X	Bogliolo et al. "Specification and Validation of Distributed IP-based Designs with JavaCAD" 1999.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited

Application/Control No.

10/713,052

Applicant(s)/Patent Under
Reexamination
MOOSBURGER ET AL.

Examiner

SHAMBHAVI PATEL

Art Unit

2128

Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gajski et al. "Reuse and Protection of Intellectual Property in the SpecC System" IEEE 2000.
	V	Wen et al. "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-on-a-Chip Design" 2001.
	W	Fin et al. "A Web-CAD Methodology for IP-Core Analysis and Simulation" ACM 2000.
	X	Hauck et al. "Data Security for Web-Based CAD" IEEE Transactions on Design Automation of Electronic Systems: 1998.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.